

U.S. Department of Commerce, Patent and Trademark Office	Serial No.:
	Filing Date: January 16, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Para K. Segaram
(Use several sheets if necessary)	Group Art Unit:
"High Speed Communication System With A Feedback Synchronization Loop"	Examiner Name: Unknown
	Attorney Docket No. RA-162-1

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						

Foreign Patent Documents

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AF							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>KIMB</i>	AG	"Draft Standard for Low-Voltage Differential Signals (LVDS) for Scalable Coherent Interface (SCI)", Draft 1.3, IEEE P1596.3-1995, pages i-viii, pages 1-34 (November 1995).
<i>KIMB</i>	AH	"Media Access Control (MAC) Parameters, Physical Layer, Repeater and Management Parameters for 1000 Mb/s Operation", IEEE Draft P802.3z/D5.0, pages i-vi, pages 01.1-01.8, 04.7-04.36, 05.1-05.6, 06.1-06.2, 22.1-22.10, 30.1-30.54, 30A.1-30A.44, 30B.1-30B.8, 31B.1-31B.2, 34.1-34.4, 35.1-35.30, 36.1-36.48, 36A.1-36A.4, 36B.1-36B.2, 37.1-37.26, 38.1-38.32, 39.1-39.20, 41.1-41.28, 42.1-42.8 (May 1998).
	AI	
	AJ	

Examiner *Kim M. Burt*

Date Considered

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.